Listing of the Claims:

Claim 1 (Canceled).

Claim 2 (Currently Amended): A CDMA transmitter ~~according to claim 1, further~~ comprising:

<ins>spreading means for employing a spreading code allocated for a self-channel to spread a transmission signal, for which a time slot having a predetermined cycle and having a different timing from that of a different channel is allocated to a synchronization time slot for the self-channel, and which is synchronized with a transmission signal for the different channel and for outputting a spreading signal;</ins>

<ins>power control means for increasing, until greater than an amplitude for a spreading signal for a different period, an amplitude for a period for the synchronization time slot of the self-channel that is included in the spreading signal, and for outputting the resultant signal as a modulation signal; and</ins>

no signal power setting means for performing a no signal power setting for the period for the synchronization time slot of the different channel that is included in the modulation signal,

wherein a sync bit is inserted into the synchronization time slot.

Claim 3 (Original): A CDMA transmitter according to claim 2, wherein the power control means also serves as the no signal power setting means, and wherein to perform a no signal power setting, the power control means halts the signal input or the signal output during the period for the synchronization time slot of the different channel.

-2-

Claim 4 (Original): A CDMA transmitter according to claim 2, wherein the spreading means also serves as the no signal power setting means, and wherein, to perform the no signal power setting, the spreading means halts the spreading during the period of the synchronization time slot for the different channel.

Claim 5 (Currently Amended): A CDMA multiplex transmitter comprising:

a plurality of CDMA transmitters according to claim [[1]] 2; and

multiplexing means for superimposing modulation signals received from the CDMA transmitters.

Claim 6 (Original): A CDMA multiplex transmitter according to claim 5, further comprising:

frame processing means for forming normal signals for synchronization time slots, which are to be transmitted to the CDMA transmitters.

Claim 7 (Original): A CDMA multiplex transmitter according to claim 6, wherein the frame processing means adds a sync bit to a transmission signal for each channel, and forms a signal to be transmitted to each of the CDMA transmitters.

Claim 8 (Currently Amended): A CDMA receiver comprising:

correlation means for obtaining a correlation between a multiplex signal received from a CDMA multiplex transmitter according to claim 5, and a spreading code allocated to a respective self-channel, and for outputting a correlation signal;

-3-

synchronization timing detection means for, upon the reception of the correlation signal from the correlation means, identifying a large positive and/or negative correlation value portion included in a <u>respective</u> synchronization time slot for the <u>respective</u> self-channel;

clock regeneration means for employing the output of the synchronization timing detection means to produce a regenerated clock having a time slot cycle; and

gate means for sampling, in accordance with the regenerated clock, the correlation signal or a signal obtained by wave-shaping the correlation signal, and for reproducing a transmission signal.

Claim 9 (Original): A CDMA receiver according to claim 8, further comprising:

frame removal means for removing a sync bit from an output signal of the gate means.

Claim 10 (Currently Amended): A CDMA communication system comprising:

a plurality of CDMA transmitters according to claim [[1]] 2; and a plurality of CDMA receivers according to claim 8.

Claim 11 (Original): A CDMA communication system comprising:

a CDMA multiplex transmitter according to claim 5; and

a plurality of CDMA receivers according to claim 8.

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